

Docket No.: MUH-12796

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : MARIE DENISON ET AL.

Filed : CONCURRENTLY HEREWITH

Title : QUASI-VERTICAL SEMICONDUCTOR COMPONENT

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

S i r :

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

Murari, B. et al. (Eds.): "Smart Power ICs – Technologies and Applications", Springer Verlag, p. 20, pp. 20 and 32-36.

Respectfully submitted,



For Applicants

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FORM PTO-1449 (SUBSTITUTE) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))				Attorney Docket No.: MUH-12796 Appl. No.: Applicant: MARIE DENISON ET AL. Filing Date: September 22, 2003 Group Art Unit:			
EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
	A						
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						
FOREIGN PATENT DOCUMENT							
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES NO
	J						
	K						
	L						
	M						
	N						
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)							
		Murari, B. et al. (Eds.): "Smart Power ICs – Technologies and Applications", Springer Verlag, p. 20, pp. 20 and 32-36					
EXAMINER				DATE CONSIDERED			